

AMENDMENTS TO THE CLAIMS

1. (Currently Amended) A thin film transistor array panel comprising:
 - a gate line formed on an insulating substrate including a gate electrode;
 - a gate insulating layer formed on the gate line;
 - a semiconductor layer formed on the gate insulating layer;
 - a data line formed on the gate insulating layer and including a source electrode;
 - a drain electrode formed at least in part on the semiconductor layer;
 - a thin film transistor comprising the gate electrode, the source electrode, the drain electrode and the semiconductor layer;
 - ~~a color filter formed directly on the data line and the drain electrode and having a first opening exposing the drain electrode at least in part;~~
 - a light blocking layer provided to block light reflected from a portion of the thin film transistor formed on the color filter;
 - a plurality of color filters formed directly on the data line and the drain electrode and having a first opening exposing the drain electrode at least in part formed above the thin film transistor, wherein two of the color filters overlap each other at a location directly above the portion of the thin film transistor;
 - ~~a passivation layer formed on the color filter and the light blocking layer and having a contact hole exposing the drain electrode through the first opening of the color~~

~~filter;~~

a pixel electrode ~~formed on the passivation layer and~~ contacting the drain electrode through ~~the a~~ contact hole;

a spacer ~~formed on the passivation layer and disposed opposite~~ the light blocking layer; and

a storage conductor formed on the gate insulating layer, overlapping the gate line, and electrically contacting the pixel electrode.

2. (Previously Presented) The thin film transistor array panel of claim 1, wherein the light blocking layer comprises an organic material including a black pigment.

3. (Previously Presented) The thin film transistor array panel of claim 1, wherein the spacer comprises an organic material.

4. (Canceled)

5. (Currently Amended) The thin film transistor array panel of claim 1, wherein the color filter ~~filters have~~ has an ~~second~~ opening exposing the storage conductor at least in part ~~and the passivation layer further has a second contact hole exposing the storage conductor at least in part through the second opening~~ for connection between the storage conductor and the pixel electrode.

6. (Original) The thin film transistor array panel of claim 1, further comprising a storage electrode formed under the gate insulating layer and overlapping the pixel electrode.

7. (Canceled)

8. (Canceled)

9. (Original) The thin film transistor array panel of claim 1, wherein ~~the a~~ passivation layer is provided over the thin film transistor, the passivation layer comprising ~~comprises an~~ acrylic material or a chemical vapor deposition film having a dielectric constant smaller than 4.0.

10. (Original) The thin film transistor array panel of claim 1, wherein the semiconductor layer has substantially the same planar shape as the data lines and the drain electrodes except for a portion between the source electrode and the drain electrode.

11. (Currently Amended) A liquid crystal display comprising:

a first panel including:

a gate line formed on a substrate including a gate electrode,

a gate insulating layer formed on the gate line,

a semiconductor layer formed on the gate insulating layer,

a data line formed on the gate insulating layer and the semiconductor layer including a source electrode and a drain electrode[.];

a thin film transistor comprising the gate electrode, the source electrode, the drain electrode, and the semiconductor layer; and

~~a color filter formed directly on the data line and the drain electrode and having an opening exposing the drain electrode at least in part;~~

a light blocking layer formed on the color filter including an organic material and a black pigment;

a passivation layer formed on the light blocking layer including a contact hole; and

a pixel electrode formed on the passivation layer and contacting the drain electrode through the a contact hole;

a second panel facing the first panel and including a common electrode;

a light blocking layer provided to block light reflected from a portion of the thin film transistor including an organic material and a black pigment;

a plurality of color filters formed above the thin film transistor, wherein two of the color filters overlap each other at a location directly above the portion of the thin film transistor;

a spacer disposed formed on between the first panel and the second panel to form a gap therebetween and overlapping the light blocking layer; and

a storage conductor formed on the gate insulating layer, overlapping the gate line, and electrically contacting the pixel electrode.

12. (Canceled)

13. (Original) The liquid crystal display of claim 11, further comprising a protrusion formed on at least one of the first and the second panels, having a height smaller than the spacer, and having a slanted lateral surface.

14. (Previously Presented) A liquid crystal display comprising:

a first panel including:

a gate line formed on a substrate including a gate electrode,

a gate insulating layer formed on the gate line,

a semiconductor layer formed on the gate insulating layer,

a data line formed on the gate insulating layer and the semiconductor layer
including a source electrode and a drain electrode;

a thin film transistor comprising the gate electrode, the source electrode, the drain
electrode and the semiconductor layer;

a light blocking provided to block light reflected from a portion of the thin film
transistor layer formed directly on the data line including organic material and black
pigment,

a passivation layer formed on the light blocking layer including a contact hole, the
passivation layer comprising a film formed by chemical vapor deposition and having a
dielectric constant less than 4.0; and

a pixel electrode formed on the passivation layer and contacting the drain
electrode through the a contact hole;

a second panel facing the first panel and including a common electrode and a color filter;

and

a spacer disposed between the first panel and the second panel to form a gap therebetween and overlapping the light blocking layer, and

a plurality of color filters formed above the thin film transistor, wherein two of the color filters overlap each other at a location directly above the portion of the thin film transistor.

15. (Previously Presented) The liquid crystal display of claim 14, further comprising a protrusion having a slanted surface formed on at least one of the first and the second panels, having a height less than a height of the spacer.

16. (Currently Amended) A thin film transistor array panel as in Claim 1, ~~wherein~~ further comprising a passivation layer provided over the thin film transistor, the passivation layer comprises a-Si:C:O or a-Si:O:F.

17. (Currently Amended) A liquid crystal display as in Claim 11, ~~wherein further~~ comprising a passivation layer provided over the thin film transistor, the passivation layer comprises a-Si:C:O or a-Si:O:F.

18. (Currently Amended) A liquid crystal display as in Claim 14 ~~wherein further~~ comprising a passivation layer provided over the thin film transistor, the passivation layer comprises a-Si:C:O or a-Si:O:F.

19. (New) A liquid crystal display as in Claim 11, wherein the spacer is formed between the first panel and the second panel.

20. (New) A thin film transistor array panel as in Claim 1, wherein the light blocking layer is formed above the data line.

21. (New) A liquid crystal display as in Claim 11, wherein the light blocking layer is formed above the data line.
22. (New) A liquid crystal display as in Claim 14, wherein the light blocking layer is formed above the data line.
23. (New) A thin film transistor array panel as in Claim 1, wherein the color filters are formed on the insulating substrate.
24. (New) A liquid crystal display as in Claim 11, wherein the color filters are formed in the first panel.
25. (New) A liquid crystal display as in Claim 14, wherein the color filters are formed in the first panel.
27. (New) A liquid crystal display as in Claim 11, wherein the color filters are formed in the second panel.
28. (New) A liquid crystal display as in Claim 14, wherein the color filters are formed in the second panel.